Claim Amendments

Please amend claim 1 as follows:
Please add new claims 21- 30 as follows:

Claims as Amended

- 1. A capacitor comprising:
 - a substrate;
- a first capacitor plate layer <u>formed disposed</u> over the substrate, the first capacitor plate layer comprising a horizontally separated and <u>contiguously</u> interconnected first series of times <u>disposed</u> on a <u>dielectric layer</u>;
- a second capacitor plate layer separated from the first capacitor plate layer by a contiguous serpentine shaped capacitor dielectric layer, the second capacitor plate layer

comprising a horizontally separated and <u>contiguously</u> interconnected second series of times, <u>said second series</u> of times horizontally interdigitated between the horizontally separated and <u>interconnected</u> first series of times, <u>wherein the capacitor dielectric layer is a single serpentine conformal dielectric layer</u>.

- 2. (original) The capacitor of claim 1 wherein the second series of times is horizontally interdigitated but not vertically interdigitated with respect to the first series of times.
- 3. (original) The capacitor of claim 1 wherein the substrate is a semiconductor substrate.
- 4. (original) The capacitor of claim 1 wherein the substrate is a ceramic substrate.

- 5. (original) The capacitor of claim 1 wherein the capacitor dielectric layer is formed to a thickness of from about 20 to about 200 angstroms.
- 6. (original) The capacitor of claim 1 wherein the second capacitor plate also covers a series of Lop surfaces of the first series of times.

Claims 7-20 cancelled

- 21. (new) The capacitor of claim 1 wherein the second series of times is self-aligned with respect to the first series of times.
- 22. (new) The capacitor of claim 1 wherein where the first series of times is contiguously interconnected according to a base portion of said first series of times, said base portion and said first series times comprising a comb shape structure extending parallel to the horizontal plane of the substrate.

- 23. (new) The capacitor of claim 1 wherein the second series of times is contiguously interconnected according to a base portion of said second series of times, said base portion and said second series times comprising a comb shape structure extending parallel to the horizontal plane of the substrate.
- 24. (new) The capacitor of claim 24 wherein the second series of times is further contiguously interconnected according to an upper conductive portion disposed over the top surface of said first series of times.
- 25. (new) The capacitor of claim 1 wherein the first series of times is encapsulated on bottom and sidewall portions.
- 26. (new) The capacitor of claim 1 wherein the first series of times is encapsulated by a barrier layer on bottom and sidewall portions.

27. (new) The capacitor of claim 1 wherein the second series of times is encapsulated by a contiguous barrier layer on bottom and sidewall portions.

28. (new) The capacitor of claim 26 wherein the second series of times including the upper conductive portion is encapsulated by a contiguous barrier layer on bottom and sidewall portions.

29. (new) A capacitor comprising:

a substrate with an overlying dielectric layer;

a comb shaped first capacitor plate disposed on the dielectric layer, said first capacitor plate layer comprising a first series of times extending parallel to the substrate and contiguously interconnected at a base portion; and,

a comb shaped second capacitor plate layer separated from the first capacitor plate layer by a contiguous serpentine shaped capacitor dielectric layer, said second capacitor plate

layer comprising a second series of times extending parallel to the substrate and contiguously interconnected at a base portion, said second series of times horizontally interdigitated between the first series of times.

30. (new) A capacitor comprising:

a substrate with an overlying dielectric layer;

a comb shaped first capacitor plate disposed on the dielectric layer, said first capacitor plate layer comprising a first series of times extending parallel to the substrate and contiguously interconnected at a base portion;

a comb shaped second capacitor plate layer separated from the first capacitor plate layer by a contiguous serpentine shaped capacitor dielectric layer, said second capacitor plate layer comprising a second series of times extending parallel to the substrate and contiguously interconnected at a base portion,

said second series of times horizontally intendigitated between the first series of times; and,

wherein the second series of times is further contiguously interconnected over a top surface of said second series of times.